



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Kimoto, Hisamitsu

Serial No.: 09/850,254

Filed: May 07, 2001

Title: Semiconductor Apparatus and
Manufacturing Method Therefor

Attorney Docket No.: N13476100S

Group Art Unit: 2836

Examiner: Quinto, Kevin V.

RESPONSE TO OFFICE ACTION

5 Mail Stop Non-Fee Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

10

A. Introductory Comments

The following is submitted in response to the Office Action dated September 04, 2003,
and is currently due December 4, 2003.

15

RECEIVED
DEC 11 2003
TECHNOLOGY CENTER 2800

20

25

37 C.F.R. §1.8

I hereby certify that this correspondence is being

30

[] transmitted via facsimile to the United States Patent and Trademark Office to fax number: _____
Date of Transmittal: _____

[X] deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents,
P.O. Box 1450, Alexandria, VA 22313-1450.

35

Date of Deposit: December 3, 2003

Typed/Printed Name: Darryl G. Walker

40

Signature: D. G. Walker